

**Notice of Allowability**

Application No.

09/886,577

Applicant(s)

ROY, RICHARD S.

Examiner

Art Unit

Paul A Baker

2188

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to after final filed 22 September 2004.
2. ☒ The allowed claim(s) is/are 1-3, 6-13 and 15-20.
3. ☒ The drawings filed on 21 June 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All   b) ☐ Some\*   c) ☐ None   of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |   |
|---|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)           |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                              |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance  |
|   | 9. <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Allowable Subject Matter***

Claims 1-3,6-13 and 15-20 are allowed.

The following is an examiner's statement of reasons for allowance:

In regards to claim 1, none of the prior art of record discloses three levels of memory, each possessing data in and data out buffer blocks and a multiplexing circuit capable of tying the third level DOUT buffer block to the DIN buffer block of the first and second levels of memory for selectively loading data in a manner which substantially simultaneous relative to accessing the data in the third level. Specifically the examiner was unable to find in the prior art of record the structure claimed by claim 1 where data from the third level memory could be loaded into the first level memory (bypassing second level memory) within a clock cycle. Therefore claim 1 is found allowable over the prior art of record, given the claimed structure and method of operation.

Claims 2-3 and 6-10 are allowable as dependent upon claim 1.

The phrase "substantially simultaneous" had been deemed definite, since this phrase is given in the context of memory access time. Specifically "substantially simultaneous" has been interpreted as meaning "an operation which occurs in less than one memory clock cycle from the triggering event". In this context, the loading from 3<sup>rd</sup>

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level DOUT into either the 1<sup>st</sup> or 2<sup>nd</sup> DIN occurs less than one memory cycle after the accessing of the data in the third level memory.

In regards to claim 11, none of the prior art of record discloses the loading operation into the first level memory is triggered by an address calculated by an separate address logic circuit in combination of the other specified claim limitations.

Claims 12,13,15-20 are allowable as dependent upon claim 11.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul A Baker whose telephone number is (571)272-24203. The examiner can normally be reached on M-F 10am-6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on (571)272-4210. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PB



Mano Padmanabhan  
10/28/04

**MANO PADMANABHAN**  
**SUPERVISORY PATENT EXAMINER**